

## GFF Pinout Table

Sigfox/LoRa	xE910	Pin		xE910	Sigfox/LoRa
VBAT	VBAT	1	2	VBAT	VBAT
VBAT	VBAT	3	4	VBAT	VBAT
GND	GND	5	6	GND	GND
GND	GND	7	8	NC	NC
NC	NC	9	10	NC	NC
NC	NC	11	12	NC	NC
NC	NC	13	14	NC	NC
NC	NC	15	16	NC	NC
NC	NC	17	18	SIMVCC	NC
NC	SIMRST	19	20	SIMIO	NC
NC	SIMIN	21	22	SIMCLK	NC
NC	RX_TRACE/SPI_MOSI	23	24	TX_TRACE/SPI_MISO	NC
TXD	C103/TXD	25	26	C104/RXD	RXD
NC	C107/DSR	27	28	C106/CTS	NC
NC	C108/DTR	29	30	C125/RING	NC
NC	C105/RTS	31	32	C109/DCD	NC
I2C_SCL/NC	NC	33	34	NC	I2C_SDA/NC
USB_ID/NC	NC	35	36	DVI_CLK	NC
ADC1	ACD_IN1	37	38	NC	NC
NC	NC	39	40	NC	NC
NC	SPI_MRDY	41	42	SPI_SRDY	NC
NC	SPI_CLK	43	44	SPI_CS_N(NC)	NC
STAT_LED	STAT_LED	45	46	GND	GND
NC	NC	47	48	USB_VBUS	USB_VBUS/NC
PWR_MON	PWR_MON	49	50	VAUX	VAUX1
NC	NC	51	52	NC	NC
ON_OFF	ON_OFF	53	54	RESET	RESET
NC	VRTC	55	56	NC	NC
NC	NC	57	58	NC	NC
DIO4	GPIO_04	59	60	NC	NC
NC	NC	61	62	NC	NC
NC	GPIO_10/DVI_TX	63	64	NC	NC
NC	DVI_RX	65	66	GPIO_03	DIO3
DIO8	GPIO_08	67	68	GPIO_06	DIO6
NC	NC	69	70	GPIO_01	DIO1
NC	DVI_WA0	71	72	NC	NC
DIO7	GPIO_07	73	74	GPIO_02	DIO2
NC	NC	75	76	GPIO_09	DIO9
NC	NC	77	78	GPIO_05	DIO5
USB_D+	USB_D+/NC	79	80	USB_D-	USB_D-/NC